

Notice of References Cited

Application/Contr I No.

09/255,777

Applicant(s)/Patent Under
Reexamination
YAMAZAKI ET AL.

Examiner

Richard A. Booth

Art Unit

2812

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U.S. PATENT DOCUMENTS

*		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	DOCUMENT SOURCE **	
							APS	OTHER
<input type="checkbox"/>	A	5064775 ✓	Nov. 1991	Chang	--	--	<input type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	B	5397718 ✓	Mar. 1995	Furuta et al.	--	--	<input type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	C	4318216 ✓	Mar. 1982	Hsu	--	--	<input type="checkbox"/>	<input type="checkbox"/>
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NON-PATENT DOCUMENTS

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			APS	OTHER
<input type="checkbox"/>	U	Wolf et al., "Silicon Processing for the VLSI Era Volume 1: Process Technology", pages 175-180, 323-325, 1986, Lattice Press.	<input type="checkbox"/>	<input type="checkbox"/>
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*A copy of this reference is not being furnished with this Office action. (See Manual of Patent Examining Procedure, Section 707.05(a).)

**APS encompasses any electronic search i.e. text, image, and Commercial Databases.

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